

AMENDMENTS TO THE SPECIFICATION:

Page 23, replace the paragraph, beginning on line 24, bridging pages 23 and 24, with the following amended paragraph:

--More specifically, the first OPC process is performed from the most inner circumferential side of the power calibration area 101P-2 by using an area PC2-1, and the second OPC process is performed by using an area PC2-2, and the third OPC process is performed by using an area PC2-3, sequentially. As described above, in the L1 layer, the power calibration area ~~101P-2~~ 103-2 is consumed, i.e. damaged, toward the outer circumferential side by the OPC process. Incidentally, in each time of the OPC process in the L1 layer, the test writing of the OPC pattern and the reproduction of the test-written OPC pattern are performed in the direction of the second track path TP2.--

Page 29, replace the paragraph, beginning on line 17, with the following amended paragraph:

--More specifically, as shown in FIG. 4, the first recording and reproduction of the control information are performed from the inner circumferential side of the recording management area 104-1 by using an area MD1-1, and the recording and reproduction of the control information are performed by using an area MD1-2, and the third recording and reproduction of the control information are performed by using an area MD1-3, sequentially. As described above, in the L0 layer, the recording

management area ~~101M-1~~ 104-1 is used toward the outer
circumferential side.--